

8:30 am Come Together
Company Presentations Saal 2

9:00 am Heinz Martin Esser, Silicon Saxony e.V.
"The Silicon Saxony - The Leading European Network for Semiconductor Technology"

Exhibition
 Leading EDA vendors present their tools and solutions for applications and issues like that discussed in edaForum08's technical and business sessions and provide hands-on advice for today's design projects.

12:00 pm Lunch
Welcome Address Saal 1

1:15 pm Wolfgang Rosenstiel, edacentrum

1:30 pm General Keynote Saal 1

Simon Yang, Chartered Semiconductor Manufacturing:
"Design and Process Interactions at the 32nm Node and Virtual IDM Environment"

Technical Session I

"WHAT A WONDERFUL WORLD"
More (Green) Chips for Less CO₂

Chairman: Wolfgang Rosenstiel

Keynote Saal 1

Gerd Teepe, AMD Saxony:
"Performance per Watt - Mission for Architecture and Design in Computing"

Business Session I

"GO YOUR OWN WAY"
More Than Moore & Less Moore for Commercial Success

Chairman: Peter van Staa

Keynote Saal 2

Pietro Perlo, Fiat:
"Energy Savings by Essential System Integration in the Forthcoming E-Mobility"

3:20 pm Coffee Break

4:00 pm Volker Kiefer, Qimonda:
"Energy Efficiency in DRAM"

C. Contiero, STMicroelectronics:
"Progress and Challenges in Semiconductor Mixed Technologies for Smart Power Applications"

4:30 pm Piet Demeester, Ghent University:
"ICT: A Big Energy Consumer"

S. Guttowski, Fraunhofer IZM:
"Technology Aware Modelling for Automation in Physical Design of Heterogeneous Systems"

5:00 pm Gerhard Fettweis, TU Dresden:
"How to Design Really Low-Power Cellular Systems"

Christian Sebeke, Bosch:
"As good as it gets - How much more EDA needs 'More than Moore'?"

5:30 pm Break
Social Event Salon Europa

7:00 pm **Baroque Dinner**
 6:45 pm: Meeting point at hotel reception

Friday Keynote Saal 1

8:45 am Daniel D. Gajski, University of California at Irvine:
"New Strategies for System Design"

Technical Session II

"IT'S MY LIFE"
More (Robust) Chips for no Failures During Lifetime

Chairman: Erich Barke

Keynote Saal 1

S. Mitra, Stanford University:
"Globally Optimized Robust System Design"

Business Session II

"LET'S TWIST AGAIN"
More Future in Microelectronics

Chairman: Jürgen Haase

Keynote Saal 2

Brian Matas, IC Insights:
"IC Industry Review and Forecast - Light at the End of the Tunnel"

Coffee Break

A. Burdett, Toumaz Technology:
"The Sensium™: Designing a Robust and Reliable Ultra-Low Power System-on-Chip for Wireless Medical Monitoring"

Barbara Schaden, Infineon:
"How About the Linkage Between Microelectronics and Global Economy?"

Christoph Heer, Infineon:
"Future Robust Systems Require Holistic Approaches Combining Hardware and Software Concepts"

Ulrich Schaefer, Bosch:
"Limits to IC Market Development: Product Development"

Panel Discussion Saal 1

11:55 am **More EDA for a Better Future in Microelectronics**
 Moderator: Peggy Aycinena, EDA Confidential

Closing Words Saal 1

12:55 pm Erich Barke, edacentrum

Lunch 1:00 pm
Trips & Tours 2:30 pm - 5:00 pm

Detailed information on page 28/29

Registration Opening Time Foyer

1st day:	2nd day:
8:00 am	8:15 am
5:30 pm	2:00 pm

EDA Lounge Foyer

Aiming to create an area for communication in a relaxed atmosphere, the EDA Lounge will be a meeting place inviting to talk to colleagues, partners and company representatives. The EDA Lounge will accompany the whole event.